

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants: N. KANEKAWA, et al  
Serial No.: Not yet assigned  
Filed: February 26, 2004  
For: ELECTRONIC CIRCUIT PACKAGE  
Group: 2818  
Examiner: V. Nguyen

**LETTER CLAIMING RIGHT OF PRIORITY**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

February 26, 2004

Sir:

Under the provisions of 35 USC 119 and 37 CFR 1.55, applicants hereby claim the right of priority based on:

**Japanese Application No. 3-034038**  
**Filed: February 28, 1991**

A Certified copy of said application document was filed in parent application Serial No. 07/843,234, filed February 28, 1992, now U.S. Patent No. 5,468,992, said certified copy filed in the United States Patent and Trademark Office on February 28, 1992.

Acknowledgement thereof is respectfully requested.

Respectfully submitted,



\_\_\_\_\_  
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CIB/jdc  
Enclosures  
703/312-6600

## 日 本 国 特 許 庁

PATENT OFFICE  
JAPANESE GOVERNMENT

紙添付の書類に記載されている事項は下記の出願書類に記載されて  
事項と同一であることを証明する。

is to certify that the annexed is a true copy of the following application as filed  
is Office.

願 年 月 日

of Application:

1991年 2月28日

願 番 号

Application Number:

平成 3年特許願第034038号

願

人

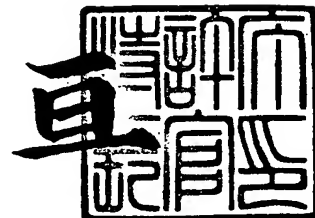
Applicant(s):

株式会社日立製作所  
日立化成工業株式会社

1991年12月13日

特許庁長官  
Commissioner,  
Patent Office

深 沢



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出証平 03-109169